

1N5820 - 1N5822

Features

- 3.0 ampere operation at T_A = 95°C with no thermal runaway.
- For use in low voltage, high frequency inverters free wheeling, and polarity protection applications.



DO-201AD COLOR BAND DENOTES CATHODE

Schottky Rectifiers

Absolute Maximum Ratings* T_A = 25°C unless otherwise noted

Symbol	Parameter		Value		
		1N5820	1N5821	1N5822	
V _{RRM}	Maximum Repetitive Reverse Voltage	20	30	40	V
I _{F(AV)}	Average Rectified Forward Current 3/8 " lead length @ $T_A = 95^{\circ}C$		3.0		
I _{FSM}	Non-repetitive Peak Forward Surge Current 8.3 ms Single Half-Sine-Wave		80		А
T _{stg}	Storage Temperature Range		-65 to +125		°C
TJ	Operating Junction Temperature		-65 to +125		°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Thermal Characteristics

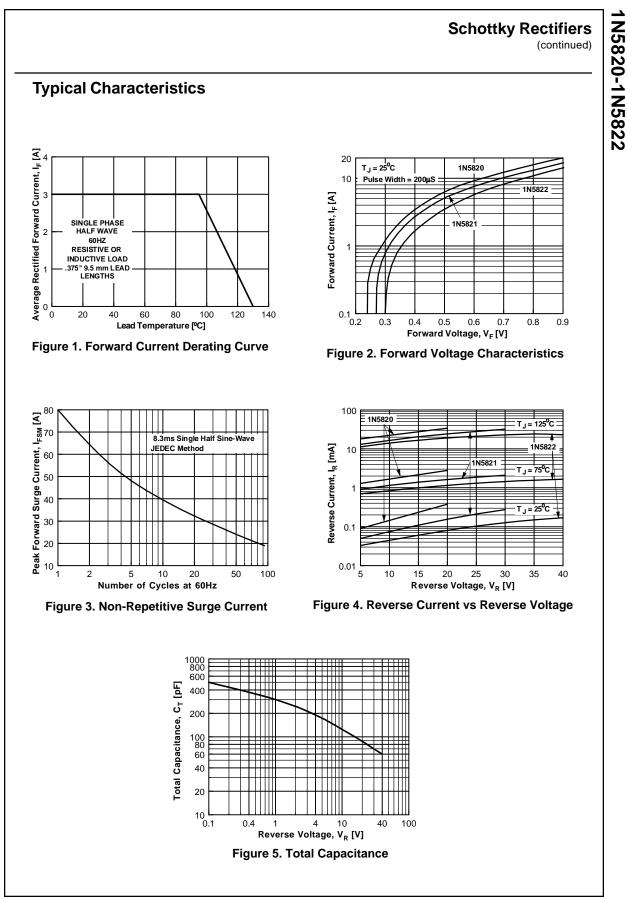
Symbol	Parameter	Value	Units
P _D	Power Dissipation	3.6	W
$R_{ ext{ hetaJA}}$	Thermal Resistance, Junction to Ambient	28	°C/W

Electrical Characteristics T_A = 25°C unless otherwise noted

Symbol	Parameter		Device		
		1N5820	1N5821	1N5822	Units
V _F	Forward Voltage @ 3.0 # @ 9.4 #	475 850	500 900	525 950	mV mV
I _R	Reverse Current @ rated V_R $T_A = 25$ $T_A = 10$		0.5 20	,	mA mA
C _T	Total Capacitance $V_{R} = 4.0 \text{ V}, \text{ f} = 1.0 \text{ MHz}$		190		pF

©2001 Fairchild Semiconductor Corporation

1N5820-1N5822



©2001 Fairchild Semiconductor Corporation

1N5820-1N5822, Rev. C

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™ Bottomless™ CoolFET™ CROSSVOLT™ DenseTrench™ DOME™ **EcoSPARK™** E²CMOS[™] EnSigna™ FACT™ FACT Quiet Series™ FAST ® FASTr™ FRFET™ GlobalOptoisolator[™] POP[™] GTO™ HiSeC™ ISOPLANAR™ LittleFET™ MicroFET™ MicroPak™ MICROWIRE™

OPTOLOGIC™ OPTOPLANAR™ PACMAN™ Power247™ PowerTrench[®] QFET™ QS™ QT Optoelectronics[™] Quiet Series[™] SILENT SWITCHER®

SMART START™ VCX™ STAR*POWER™ Stealth™ SuperSOT[™]-3 SuperSOT[™]-6 SuperSOT[™]-8 SyncFET™ TinyLogic™ TruTranslation[™] UHC™ UltraFET[®]

STAR*POWER is used under license

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY. FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.
	In Design First Production Full Production